



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of

Shao-Pin RU

Serial No.: 10/670,416

Filed: September 25, 2003

For: METHOD OF FABRICATING
A THIN FILM INTEGRATED
CIRCUIT WITH THICK FILM
RESISTORS (As Amended)

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Group Art Unit: 3729

Examiner: Phan, Thiem D.

02/28/2006 CNEGA1 00000056 10670416

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I hereby certify that this correspondence is being
deposited with the United States Postal Service as first
class mail in an envelope addressed to Mail Stop
Amendment, Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450

William E. Pelton
William E. Pelton
Reg. No. 25,702

February 24, 2006
Date

**AMENDMENT AND PETITION FOR
A ONE-MONTH EXTENSION OF TIME**

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

S I R:

This Amendment is submitted in response to the Office Action issued November 1, 2005 in connection with the above-identified application. A response to the November 1, 2005 Office Action was due February 1, 2006. Applicants hereby request a one-month extension of time from February 1, 2006 to March 1, 2006 for responding to the November 1, 2006 Office Action.

The required fee for the one-month extension of time is \$60.00 for a small entity and a check including this amount is enclosed. Therefore, a response to the November 1, 2006 Office Action is now due March 1, 2006 and this Amendment is being timely filed.

Kindly amend the above-identified application as follows:

AMENDMENT TO THE TITLE

Kindly change the title of the invention to read

--Method of Fabricating a Thin Film Integrated Circuit With Thick Film Resistors--.